LESSON PLAN	
JHARSUGUDA ENGINEERING SCHOOL,JHARSUGUDA	
Name of the Faculty: Lipsa Panigrahi	Academic Year: 2019-20
Course No.: ETT 521	Course name: Microprocessor and it's interfacing
Programme : Diploma	Branch: Electrical
Year/Sem: 3rd/ 5th	Section:

Sl. No.	Period	Time (min)	Unit	Topic to be Covered	Teaching Method
1.	1.	55 min	1	Introduction to microprocessor and micro computer	Black board
2.	2.	55min	1	Evaluation of microprocessor	Black board
3.	3.	55min	1	Advantage and application of microprocessor	Black board
4.	4.	55min	2	Architecture of intel 8085A microprocessor	Black board
5.	5.	55min	2	Functional block diagram of 8085A microprocessor	Black board
6.	6.	55min	2	Interface section	Black board
7.	7.	55min	2	Different types of buses in 8085 microprocessor	Black board
8.	8.	55min	2	Pin diagram and description	Black board
9.	9.	55min	2	Clock pulse generation and reset circuit	Black board
10	10.	55min	3	Execution timing instructions	Black board
11	11.	55 min	3	Addressing modes	Black board
12	12.	55 min	3	Grouping of instructions	Black board
13	13.	55min	3	Explanation of different group instructions with example	Black board
14	14.	55 min	3	8085A timing states	Black board
15	15.	55 min	3	Instruction fetching and execution	Black board
16	16.	55min	3	Timing diagram of different machine cycle	Black board
17	17.	55min	3	Effect of addressing mode on execution timing	Black board
18	18.	55 min	3	Condition flags	Black board
19	19.	55 min	4	Assembly language	Black board
20	20.	55 min	4	Hand assembler and Cross assembler	Black board
21	21.	55 min	4	One pass and two pass assembler	Black board
22	22.	55min	4	Advantage of assembly and high level language	Black board
23	23.	55min	4	Operating system software	Black board
24	24.	55 min	4	Modular and structure programming	Black board
25	25.	55 min	4	Counter and time delay	Black board
26	26.	55 min	4	Stack and Sub routine	Black board
27	27.	55 min	4	Example of assembly language programming	Black board
28	28.	55 min	5	Primary memory	Black board
29	29.	55 min	5	Secondary memory	Projector

30	30.	55 min	5	Internal organization of RAM and ROM	Projector
31	31.	55 min	5	Addressing memory location	Black board
32	32.	55 min	5	Chip select generation of memory	Black board
33	33.	55 min	5	I/O port addressing	Projector
34	34.	55 min	5	Generation of chip select	Projector
35	35.	55 min	6	Functional block diagram of 8255	Black board
36	36.	55 min	6	Operation and programming of 8255	Black board
37	37.	55 min	6	Functional block diagram of 8253	Black board
38	38.	55 min	6	Operational modes and programming of 8253	Black board
39	39.	55 min	6	Priority interrupt controller INTEL-8259	Black board
40	40.	55 min	6	Functional block diagram and description of blocks	Black board
41	41.	55 min	6	Programming of 8259	Black board
42	42.	55 min	6	Serial communication and (USART) INTEL-8251	Black board
43	43.	55 min	6	Methods of communication	Black board
44	44.	55 min	6	Functional block diagram and description of blocks of INTEL-8251	Black board
45	45.	55 min	6	Programming of 8251	Black board
46	46.	55 min	7	DA converter specification	Black board
47	47.	55 min	7	AD converter specification	Black board
48	48.	55 min	7	AD output codes	Black board
49	49.	55 min	7	The DAC 0808 principle of operation	Black board
50	50.	55 min	7	Application of DAC for speed control of DC motor	Black board
51	51.	55 min	7	The ADC 0801 principle of operation with example	Black board
52	52.	55 min	8	Digital clock	Black board
53	53.	55 min	8	Traffic light controller	Black board
54	54.	55 min	8	Communication models	Black board
55	55.	55 min	8	RAM	Black board
56	56.	55 min	8	PROM	Black board
57	57.	55 min	8	EPROM	Black board
58	58.	55 min	8	EEPROM	Black board
59	59.	55 min		Revision of all topics	Black board
60	60.	55 min		Revision of all topics	Black board