

**DEPARTMENT OF ELECTRONICS AND TELECOMMUNICATION ENGINEERING**  
**REVISED TIME TABLE FOR 3<sup>RD</sup>, 5<sup>TH</sup> SEMESTER WINTER 2022-23 w.e.f. 01.11.2022**

DAYS	SEM	1 <sup>st</sup> (10:00-10:55AM)	2 <sup>nd</sup> (10:55-11:50AM)	3 <sup>rd</sup> (11:50-12:45PM)	4 <sup>th</sup> (12:45 -1:40PM)		5 <sup>th</sup> (2:15-3:10PM)	6 <sup>th</sup> (3:10-4:05PM)	7 <sup>th</sup> (4:05-5:00PM)
MONDAY	5 <sup>th</sup> ETC	P.ETC (YN)	ADC(RD)	WP (SP)	EMST(YN)	R	VLSI(RB)	← PROJECT LAB(YN) →	
	3 <sup>rd</sup> ETC	EVS (RB)	← CKT SIM LAB(RB) →		MATHS(BM)		← C.PROG & WD LAB →		
	5 <sup>th</sup> ETC	P.ETC(YN)	← ADC LAB (RD) →				EMST(YN)	SCA(YN)	
TUESDAY	3 <sup>rd</sup> ETC	DETC (RD)	EVS (RB)	← EMI LAB → (YN)		E	CKT.TH (AM)	SCA(RD)	SCA(RD)
	3 <sup>rd</sup> IT	DETC (SP)		DETC (SP)					
	5 <sup>th</sup> ETC	VLSI(RB)	ADC (RD)	WP (SP)	P.ETC(YN)		← P.ETC LAB(YN) →		
WEDNESDAY	3 <sup>rd</sup> ETC	D.ETC(RD)	EMI (YN)	EVS(RB)	MATHS(BM)	C	CKT TH (AM)	← D.ETC LAB(RD) →	
	3 <sup>rd</sup> IT	DETC (SP)							
	5 <sup>th</sup> ETC	WP(SP)	ADC (RD)	P.ETC (YN)	VLSI(RB)		EMST(YN)	← PROJECT LAB(RD) →	
THURSDAY	3 <sup>rd</sup> ETC	EMI (YN)	← CKT SIM LAB (RB) →		MATHS(BM)	S	CKT TH (AM)	← EMI LAB (YN) →	
	3 <sup>rd</sup> IT		DETC (SP)		D.ETC LAB(SP)				
	5 <sup>th</sup> ETC	EMST(YN)	VLSI (RB)	WP (SP)	ADC (RD)		SCA(YN)	← WP LAB(SP) →	
FRIDAY	3 <sup>rd</sup> ETC	MATHS(BM)	EMI (YN)	DETC (RD)	EVS(RB)	S	← D.ETC LAB (RD) →		SCA(RD)
	3 <sup>rd</sup> IT								
	5 <sup>th</sup> ETC	← VLSI LAB (RB) →		→					
SATURDAY	3 <sup>rd</sup> ETC	← C.PROG & WD LAB →		EMI (YN)	WP LAB(SP)				
	3 <sup>rd</sup> IT	← DETC LAB (SP) →			DETC (RD)				
	5 <sup>th</sup> IT								

Memo no. 687 /ETC/IES Dt.31.10.2022  
 Copy to Department Notice Board and Institution Notice Board for information of students.

Memo no. 687 /ETC/IES Dt.31.10.2022  
 Copy to Academic co-ordinator/ Sr. Lecturer IT, Math & Science for Information & necessary action.

HOD(E&TC)  
 IES, IHARSUGUDA

PRINCIPAL  
 31/10/22  
 IES, IHARSUGUDA