

**DEPARTMENT OF ELECTRONICS AND TELECOMMUNICATION ENGINEERING**

**REVISED TIME TABLE FOR 3<sup>RD</sup>, 5<sup>TH</sup> SEMESTER OF FOR WINTER 2020-21** w.e.f. 17/02/2021

DAYS	SEM	1 <sup>ST</sup> (10:00-10:55AM)				2 <sup>ND</sup> (10:55-11:50AM)				3 <sup>RD</sup> (11:50-12:45PM)				4 <sup>TH</sup> (12:45-1:40PM)				5 <sup>TH</sup> (2:15-3:10PM)				6 <sup>TH</sup> (3:10-4:05PM)				7 <sup>TH</sup> (4:05-5:00PM)					
		5 <sup>TH</sup> ETC		5 <sup>TH</sup> IT		3rd ETC		3rd IT		5 <sup>TH</sup> ETC		5 <sup>TH</sup> IT		3rd ETC		3rd IT		5 <sup>TH</sup> ETC		5 <sup>TH</sup> IT		3rd ETC		3rd IT		5 <sup>TH</sup> ETC		5 <sup>TH</sup> IT		3rd ETC	
MONDAY	5 <sup>TH</sup> ETC	ADC(SP)		ADC(SP)		WP (RD)		EMST(YN)		VLSI(JN)		← project lab → (YN)		EVS(RD)		← CRT SIM LAB → (RD)		SCA		P.ETC(YN)		EMST(YN)		EMST(YN)		EMST(YN)		EMST(YN)		EMST(YN)	
	5 <sup>TH</sup> IT	EMST(YN)		DETC (JN)		← EMI LAB → (YN)		MATHS		← VLSI LAB → (JN)		← CRT SIM LAB → (RD)		← C PROG LAB →		DETC (JN)		P.ETC(YN)		VLSI(JN)		SCA		MATHS		EVS (RD)		SCA		SCA	
	3rd ETC	DETC (JN)		← EMI LAB → (YN)		← CRT SIM LAB → (RD)		EVS(RD)		← VLSI LAB → (JN)		← CRT SIM LAB → (RD)		← C PROG LAB →		DETC (JN)		P.ETC(YN)		VLSI(JN)		SCA		MATHS		EVS (RD)		SCA		SCA	
	3rd IT	WP(RD)		← EMI LAB → (YN)		← CRT SIM LAB → (RD)		EVS(RD)		← VLSI LAB → (JN)		← CRT SIM LAB → (RD)		← C PROG LAB →		DETC (JN)		P.ETC(YN)		VLSI(JN)		SCA		MATHS		EVS (RD)		SCA		SCA	
TUESDAY	5 <sup>TH</sup> ETC	← Wave propagation Lab → (RD)		EMST(YN)		← EMI LAB → (YN)		SCA		← VLSI LAB → (JN)		← CRT SIM LAB → (RD)		← C PROG LAB →		DETC (JN)		P.ETC(YN)		VLSI(JN)		SCA		EMST(YN)		EMST(YN)		EMST(YN)		EMST(YN)	
	5 <sup>TH</sup> IT	EMST(YN)		← EMI LAB → (YN)		← CRT SIM LAB → (RD)		SCA		← VLSI LAB → (JN)		← CRT SIM LAB → (RD)		← C PROG LAB →		DETC (JN)		P.ETC(YN)		VLSI(JN)		SCA		EMST(YN)		EMST(YN)		EMST(YN)		EMST(YN)	
	3rd ETC	D.ETC(JN)		← EMI LAB → (YN)		← CRT SIM LAB → (RD)		SCA		← VLSI LAB → (JN)		← CRT SIM LAB → (RD)		← C PROG LAB →		DETC (JN)		P.ETC(YN)		VLSI(JN)		SCA		EMST(YN)		EMST(YN)		EMST(YN)		EMST(YN)	
	3rd IT	← Wave propagation Lab → (RD)		EMST(YN)		← EMI LAB → (YN)		SCA		← VLSI LAB → (JN)		← CRT SIM LAB → (RD)		← C PROG LAB →		DETC (JN)		P.ETC(YN)		VLSI(JN)		SCA		EMST(YN)		EMST(YN)		EMST(YN)		EMST(YN)	
WEDNESDAY	5 <sup>TH</sup> ETC	ADC (SP)		VLSI(JN)		P.ETC (YN)		WP(RD)		← VLSI LAB → (JN)		← CRT SIM LAB → (RD)		← C PROG LAB →		DETC (JN)		P.ETC(YN)		VLSI(JN)		SCA		EMST(YN)		EMST(YN)		EMST(YN)		EMST(YN)	
	5 <sup>TH</sup> IT	EMST (YN)		← EMI LAB → (YN)		← CRT SIM LAB → (RD)		SCA		← VLSI LAB → (JN)		← CRT SIM LAB → (RD)		← C PROG LAB →		DETC (JN)		P.ETC(YN)		VLSI(JN)		SCA		EMST(YN)		EMST(YN)		EMST(YN)		EMST(YN)	
	3rd ETC	CXT.TH (RD)		EVS (RD)		← D.ETC LAB (JN) →		← VLSI LAB → (JN)		← CRT SIM LAB → (RD)		← C PROG LAB →		DETC (JN)		P.ETC(YN)		VLSI(JN)		SCA		SCA		EMST(YN)		EMST(YN)		EMST(YN)		EMST(YN)	
	3rd IT	← Wave propagation Lab → (RD)		EMST (YN)		← CRT SIM LAB → (RD)		SCA		← VLSI LAB → (JN)		← CRT SIM LAB → (RD)		← C PROG LAB →		DETC (JN)		P.ETC(YN)		VLSI(JN)		SCA		EMST(YN)		EMST(YN)		EMST(YN)		EMST(YN)	
THURSDAY	5 <sup>TH</sup> ETC	WP (RD)		← P.ETC LAB (SP) →		← EMI LAB → (YN)		MATHS		← VLSI LAB → (JN)		← CRT SIM LAB → (RD)		← C PROG LAB →		DETC (JN)		P.ETC(YN)		VLSI(JN)		SCA		EMST(YN)		EMST(YN)		EMST(YN)		EMST(YN)	
	5 <sup>TH</sup> IT	EMST (YN)		← P.ETC LAB (SP) →		← EMI LAB → (YN)		MATHS		← VLSI LAB → (JN)		← CRT SIM LAB → (RD)		← C PROG LAB →		DETC (JN)		P.ETC(YN)		VLSI(JN)		SCA		EMST(YN)		EMST(YN)		EMST(YN)		EMST(YN)	
	3rd ETC	EMI (YN)		← P.ETC LAB (SP) →		← EMI LAB → (YN)		MATHS		← VLSI LAB → (JN)		← CRT SIM LAB → (RD)		← C PROG LAB →		DETC (JN)		P.ETC(YN)		VLSI(JN)		SCA		EMST(YN)		EMST(YN)		EMST(YN)		EMST(YN)	
	3rd IT	DETC (JN)		← P.ETC LAB (SP) →		← EMI LAB → (YN)		MATHS		← VLSI LAB → (JN)		← CRT SIM LAB → (RD)		← C PROG LAB →		DETC (JN)		P.ETC(YN)		VLSI(JN)		SCA		EMST(YN)		EMST(YN)		EMST(YN)		EMST(YN)	
FRIDAY	5 <sup>TH</sup> ETC	VLSI (JN)		← ADC LAB (SP) →		← EMI LAB → (YN)		← CRT SIM LAB → (RD)		← VLSI LAB → (JN)		← CRT SIM LAB → (RD)		← C PROG LAB →		DETC (JN)		P.ETC(YN)		VLSI(JN)		SCA		EMST(YN)		EMST(YN)		EMST(YN)		EMST(YN)	
	5 <sup>TH</sup> IT	← ADC LAB (SP) →		← EMI LAB → (YN)		← CRT SIM LAB → (RD)		← VLSI LAB → (JN)		← CRT SIM LAB → (RD)		← C PROG LAB →		DETC (JN)		P.ETC(YN)		VLSI(JN)		SCA		SCA		EMST(YN)		EMST(YN)		EMST(YN)		EMST(YN)	
	3rd ETC	CXT.TH (RD)		← ADC LAB (SP) →		← EMI LAB → (YN)		← CRT SIM LAB → (RD)		← VLSI LAB → (JN)		← CRT SIM LAB → (RD)		← C PROG LAB →		DETC (JN)		P.ETC(YN)		VLSI(JN)		SCA		EMST(YN)		EMST(YN)		EMST(YN)		EMST(YN)	
	3rd IT	← ADC LAB (SP) →		← EMI LAB → (YN)		← CRT SIM LAB → (RD)		← VLSI LAB → (JN)		← CRT SIM LAB → (RD)		← C PROG LAB →		DETC (JN)		P.ETC(YN)		VLSI(JN)		SCA		SCA		EMST(YN)		EMST(YN)		EMST(YN)		EMST(YN)	
SATURDAY	5 <sup>TH</sup> ETC	← ADC LAB (SP) →		← EMI LAB → (YN)		← CRT SIM LAB → (RD)		← VLSI LAB → (JN)		← CRT SIM LAB → (RD)		← C PROG LAB →		DETC (JN)		P.ETC(YN)		VLSI(JN)		SCA		SCA		EMST(YN)		EMST(YN)		EMST(YN)		EMST(YN)	
	5 <sup>TH</sup> IT	← ADC LAB (SP) →		← EMI LAB → (YN)		← CRT SIM LAB → (RD)		← VLSI LAB → (JN)		← CRT SIM LAB → (RD)		← C PROG LAB →		DETC (JN)		P.ETC(YN)		VLSI(JN)		SCA		SCA		EMST(YN)		EMST(YN)		EMST(YN)		EMST(YN)	
	3rd ETC	← ADC LAB (SP) →		← EMI LAB → (YN)		← CRT SIM LAB → (RD)		← VLSI LAB → (JN)		← CRT SIM LAB → (RD)		← C PROG LAB →		DETC (JN)		P.ETC(YN)		VLSI(JN)		SCA		SCA		EMST(YN)		EMST(YN)		EMST(YN)		EMST(YN)	
	3rd IT	← ADC LAB (SP) →		← EMI LAB → (YN)		← CRT SIM LAB → (RD)		← VLSI LAB → (JN)		← CRT SIM LAB → (RD)		← C PROG LAB →		DETC (JN)		P.ETC(YN)		VLSI(JN)		SCA		SCA		EMST(YN)		EMST(YN)		EMST(YN)		EMST(YN)	

Memo no. 575/ETC JETC/JES Dt. 17/02/2021  
 Copy to Department Notice Board and Institution Notice Board for information of students.

Memo no. 576/ETC JETC/JES Dt. 17/02/2021  
 Copy to Academic co-ordinator/ Sr. Lecturer IT, Math & Science for information & necessary action.

*[Signature]*  
 HOD(ETC)  
 JES, JHARSUGUDA  
 17/02/2021

*[Signature]*  
 PRINCIPAL  
 JES, JHARSUGUDA  
 17/02/2021