

**DEPARTMENT OF ELECTRONICS AND TELECOMMUNICATION ENGINEERING**  
**REVISED TIME TABLE FOR 3RD, 5TH SEMESTER OF FOR WINTER 2022-23 w.e.f.....**

DAYS	SEM	1 <sup>st</sup> (10:00-10:55AM)	2 <sup>nd</sup> (10:55-11:50AM)	3 <sup>rd</sup> (11:50-12:45PM)	4 <sup>th</sup> (12:45 -1:40PM)	5 <sup>th</sup> (2:15-3:10PM)	6 <sup>th</sup> (3:10-4:05PM)	7 <sup>th</sup> (4:05-5:00PM)
		P.ETC (YN)	WP (SP)	ADC(RD)	EMST(YN)		VLSI(RB)	← project lab → (YN)
MONDAY	5 <sup>th</sup> ETC	P.ETC (YN)	WP (SP)	ADC(RD)	EMST(YN)	VLSI(RB)	← project lab → (YN)	
	3rd ETC	DETC (RD)	← CKT SIM LAB → (RB)	→ (RB)	MATHS	CKT TH (AM)	SCA(RD)	SCA(RD)
	3rd IT			DETC (SP)		EMST(YN)	ADC (RD)	SCA(YN)
TUESDAY	5 <sup>th</sup> ETC	P.ETC(YN)	← ADC LAB (RD) →	← ADC LAB (RD) →		CKT TH (AM)	EMI (YN)	← EVS(RB) →
	3rd ETC	DETC (RD)	EVS (RB)	← EMI LAB → (YN)				
	3rd IT	DETC (SP)						
WEDNESDAY	5 <sup>th</sup> ETC	P.ETC(YN)	VLSI(RB)	ADC (RD)	WP (SP)	← P.ETC LAB (YN) →		
	3rd ETC	MATHS	← EMI LAB → (YN)	→ (YN)	D.ETC(RD)	CKT TH (AM)	← CKT SIM LAB → (RB)	
	3rd IT	DETC (SP)				EMST(YN)	← D.ETC LAB (SP) →	
THURSDAY	5 <sup>th</sup> ETC	ADC (RD)	VLSI(RB)	WP(SP)	WP(SP)	← project lab → (RD)		
	3rd ETC	EMI (YN)	MATHS	← D.ETC LAB (RD) →		CKT TH (AM)	EVS(RB)	EMI (YN)
	3rd IT							
FRIDAY	5 <sup>th</sup> ETC	P.ETC (YN)	VLSI (RB)	ADC (RD)	SCA(YN)	← Wave propagation Lab → (SP)		
	3rd ETC	DETC (RD)	EMI (YN)	EVS(RB)	SCA(RD)	MATHS	← D.ETC LAB (RD) →	
	3rd IT			DETC (SP)				
SATURDAY	5 <sup>th</sup> ETC	EMST(YN)	← VLSI LAB → (RB)					
	3rd ETC		C & WD Lab (SS/RD/RB)					
	3rd IT	← DETC LAB (SP) →						

Memo no. 678.....JETC/JES DI...15.09.2022  
 Copy to Department Notice Board and Institution Notice Board for information of students.  
 Memo no. 679.....JES DI...15.09.2022  
 Copy to Academic co-ordinator/ Sr. Lecturer IT, Electrical, Math & Science for information & necessary action.

*[Signature]*  
 HOD (E&TC)  
 JES, JHARSUGUDA

*[Signature]*  
 PRINCIPAL  
 15/9/22  
 JES, JHARSUGUDA